


LCFC Confidential

Y760 M/B Schematics Document

AMD-Cezanne-FP6_BGA1140 with DDR4 + NV N20 E3/E5 GPU

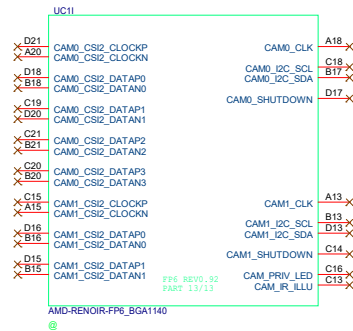
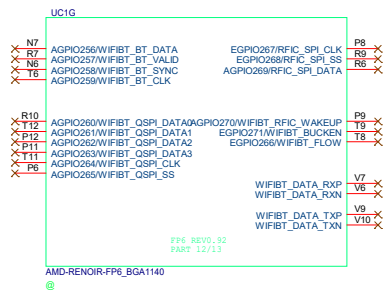
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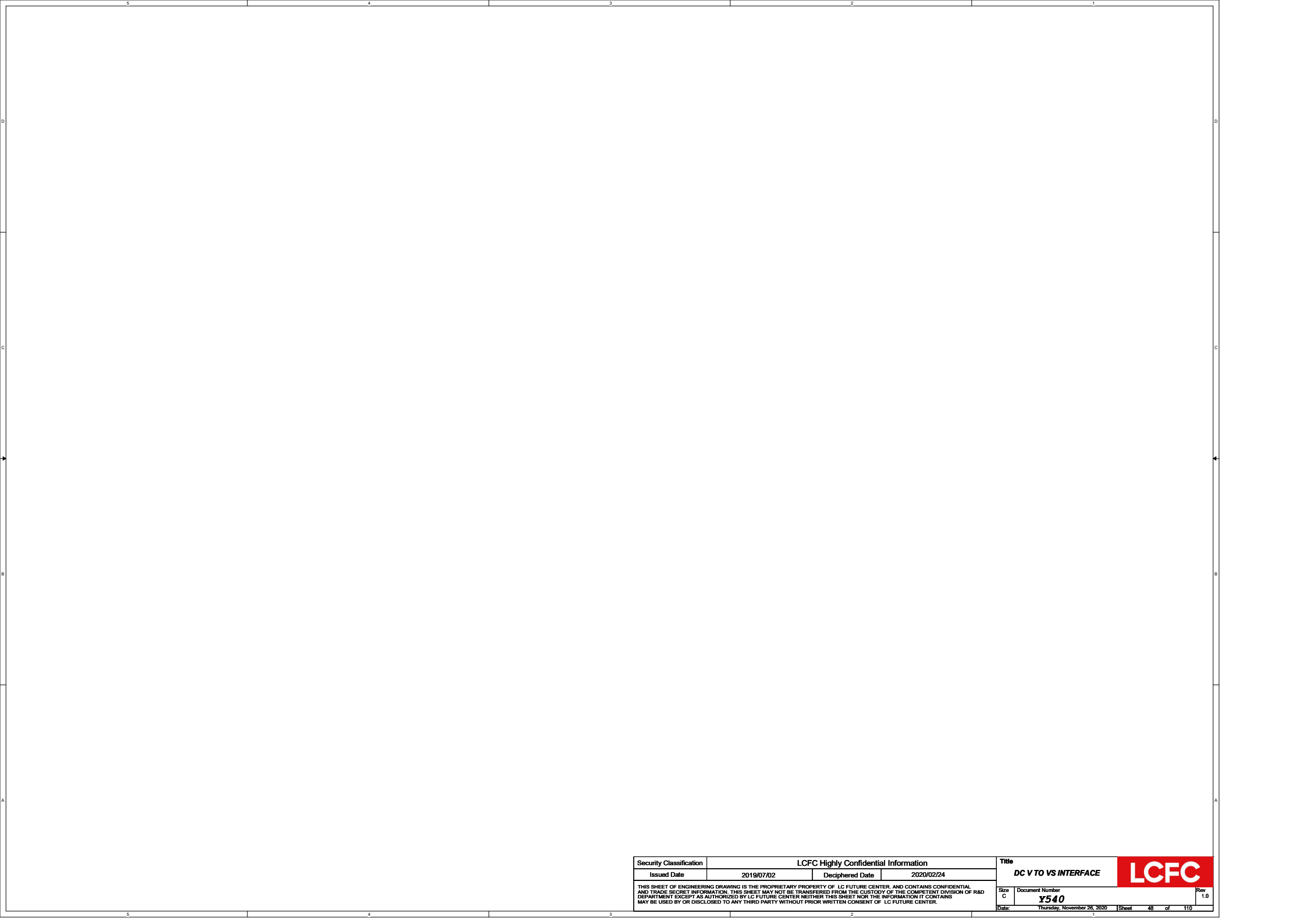
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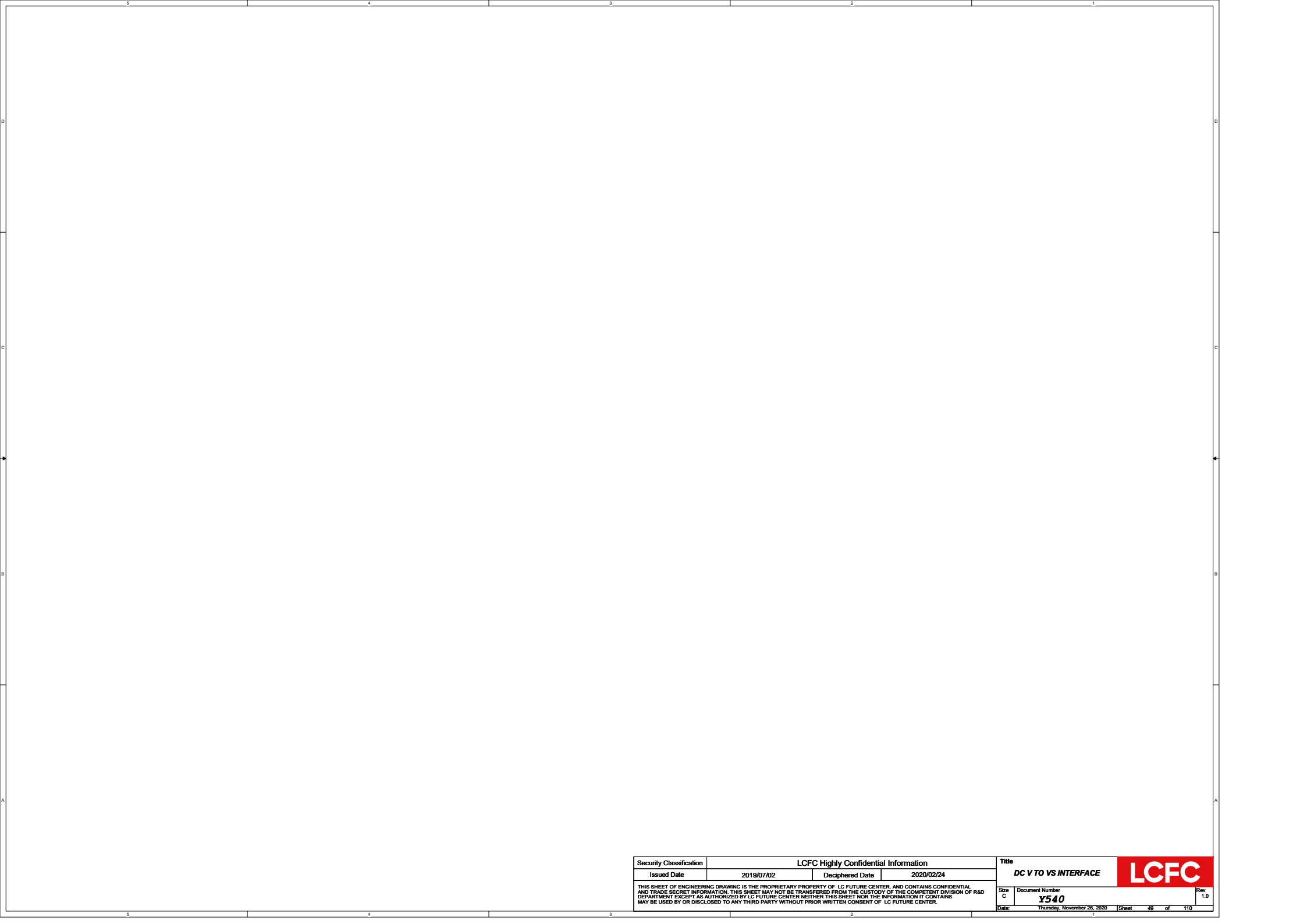
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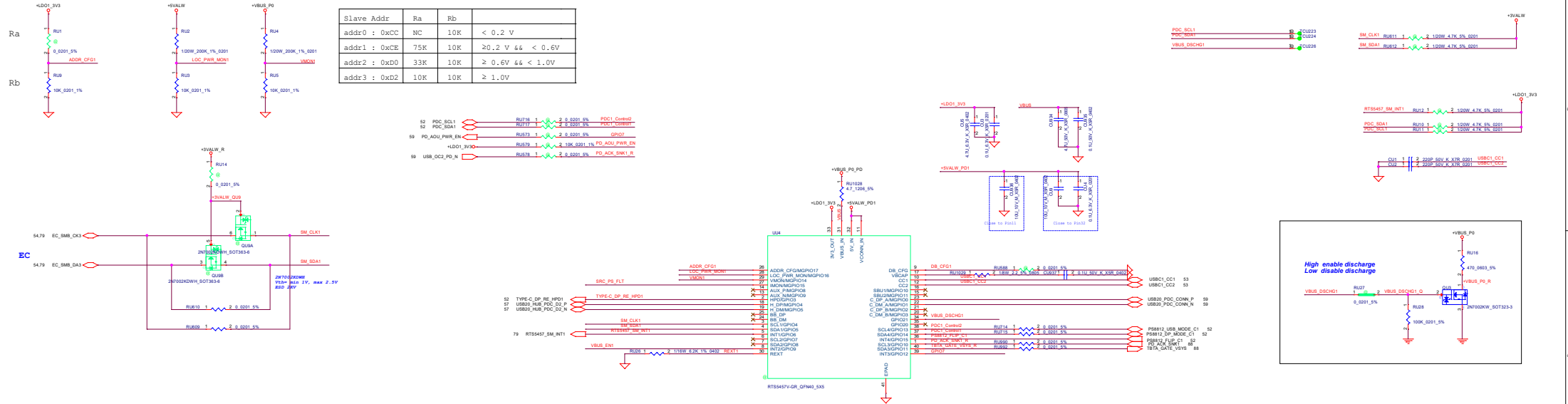




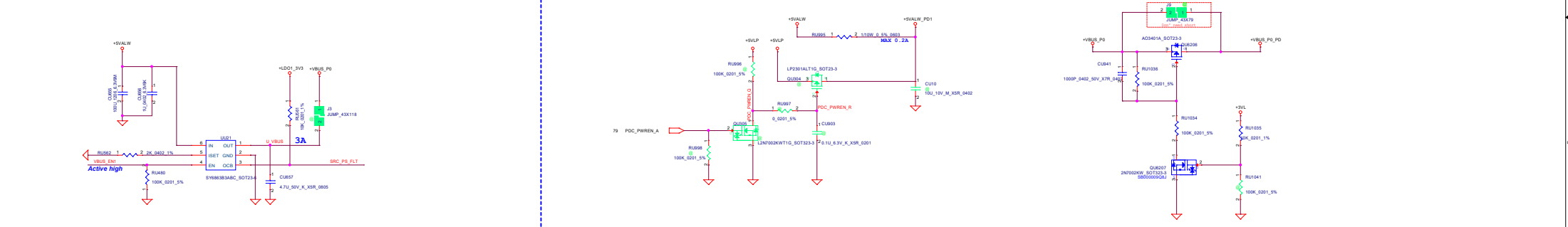
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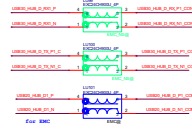
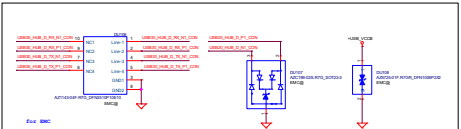
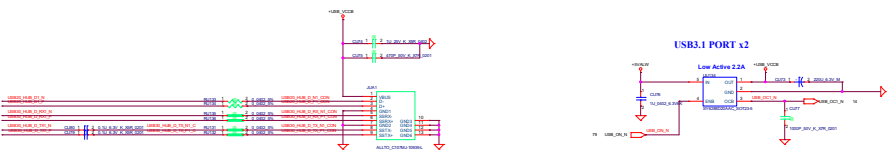


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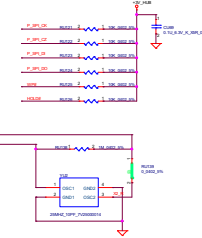
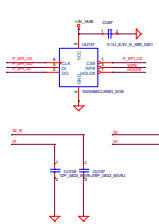
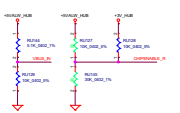
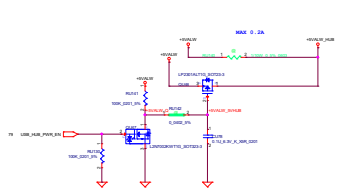
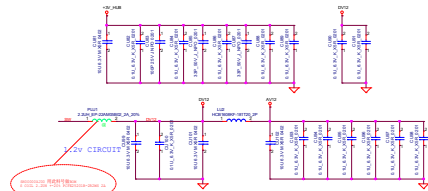
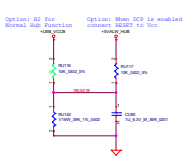
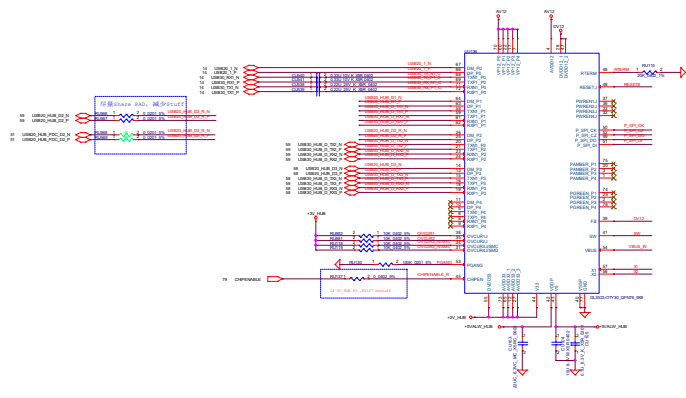


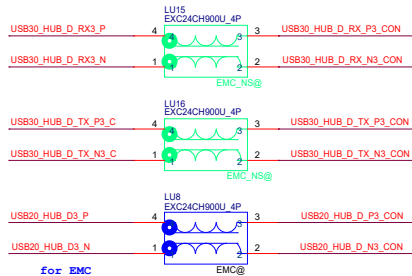
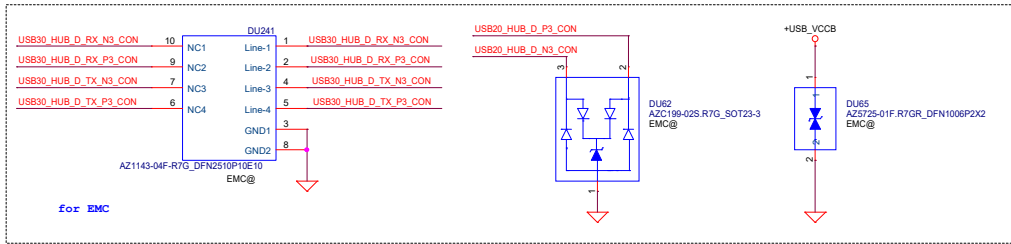
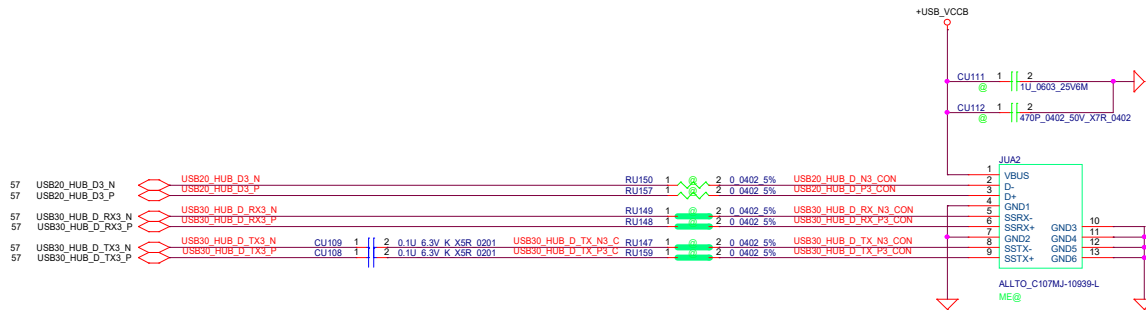
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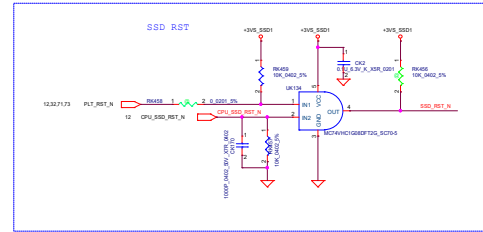
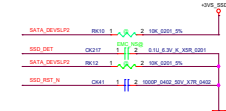
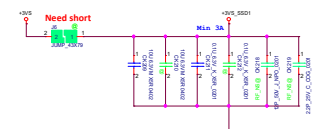
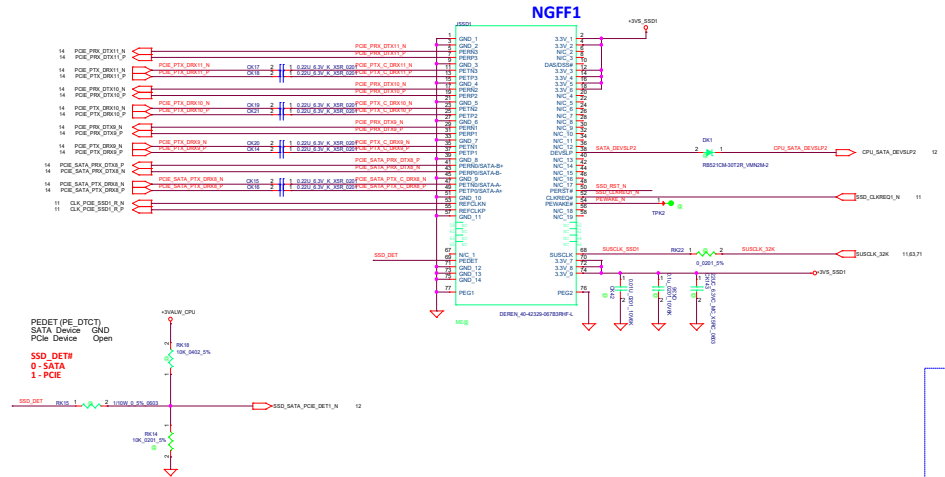


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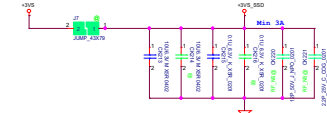
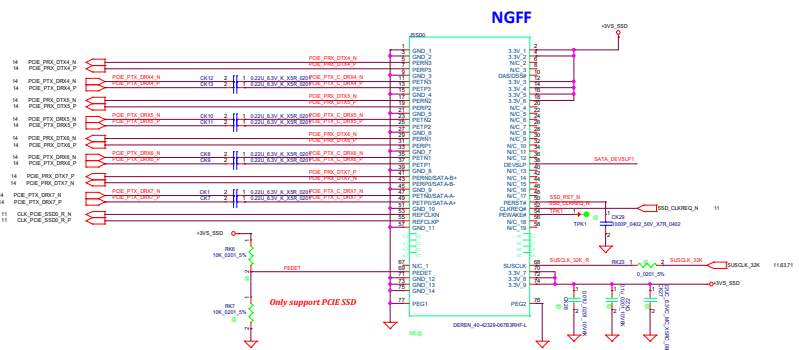
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M.2 SSD1(SATA/PCIE)



M.2 SSD0(PCIE)



Delete USB3.0 Reapter Remove to DB board

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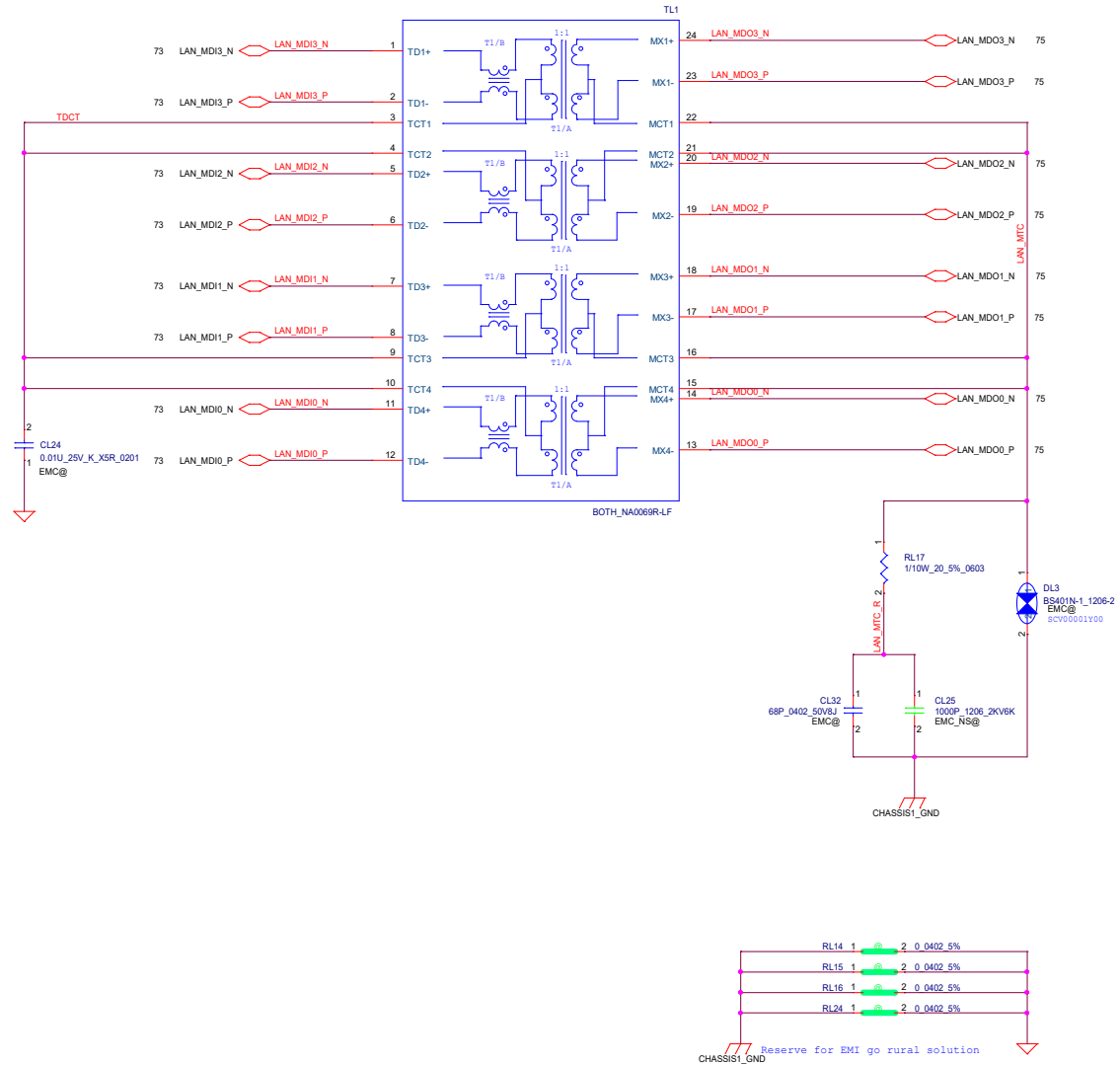
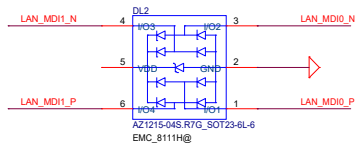
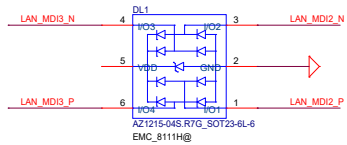
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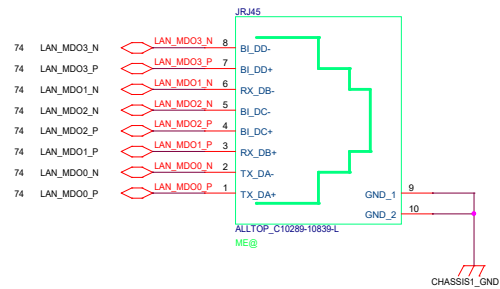
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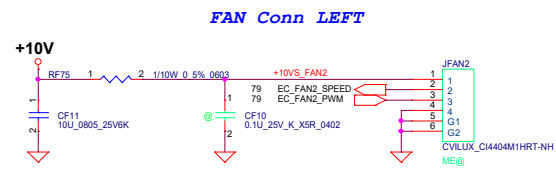
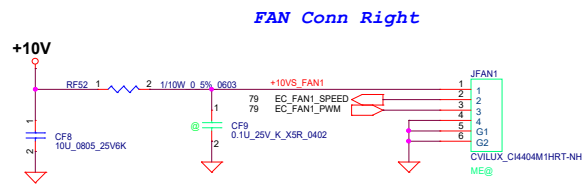


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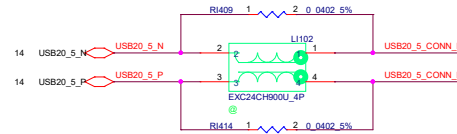
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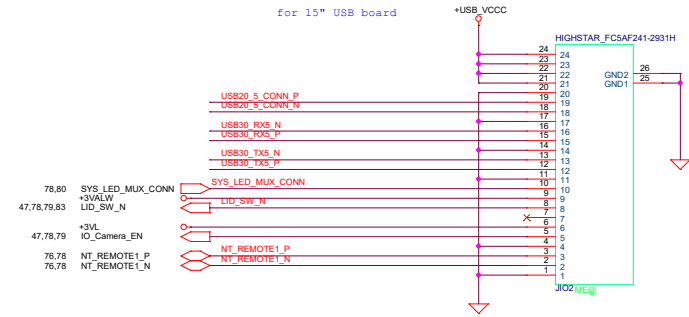
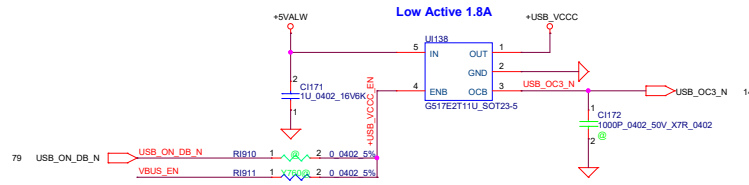
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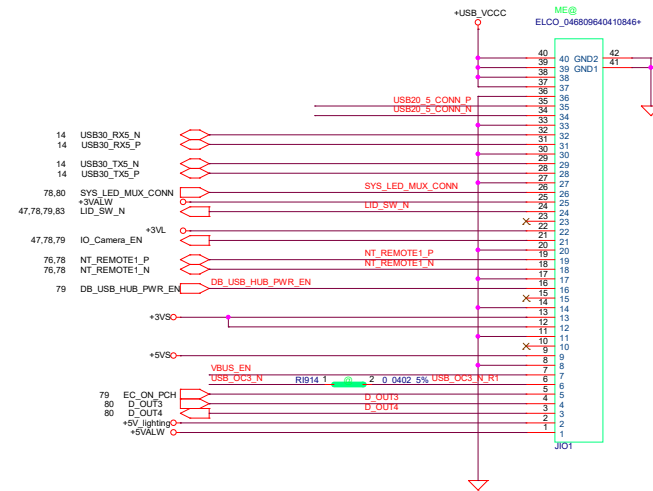
Right Side USB2.0 Port X 1 (USB/B)

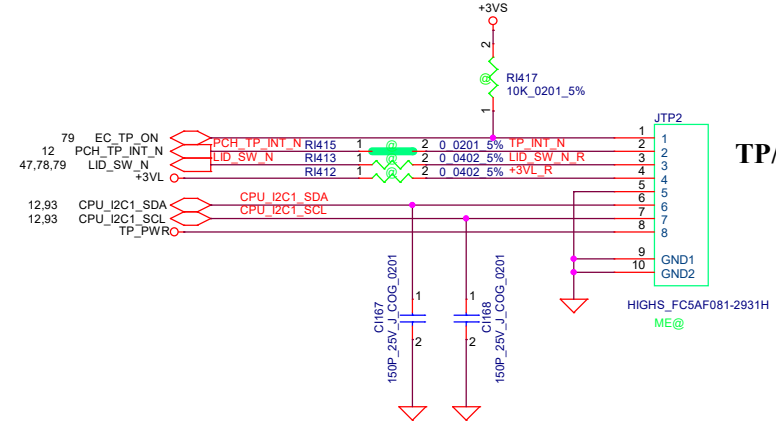
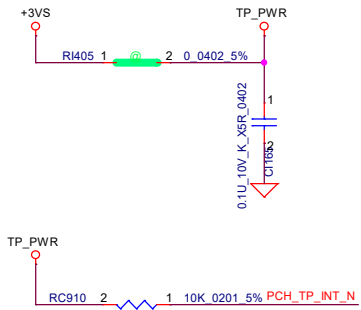
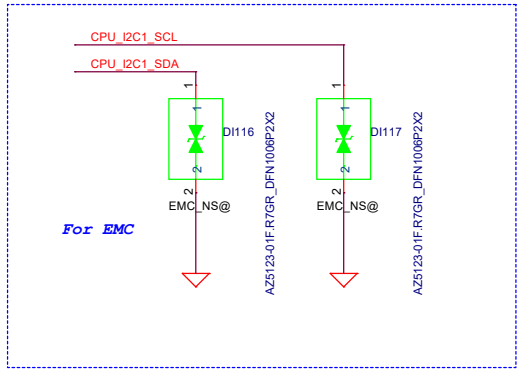


USB3.1 PORT x1

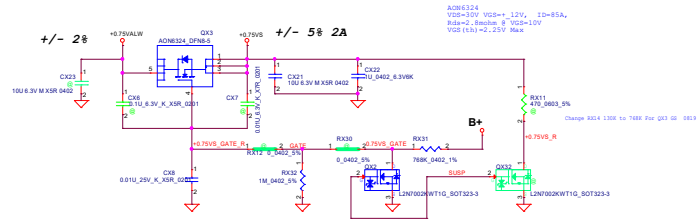
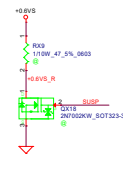
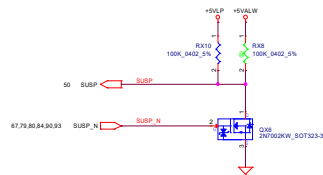
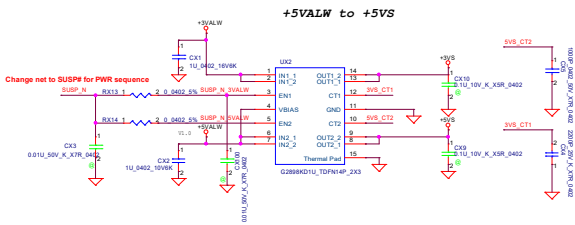


for 17" / Y760 USB board



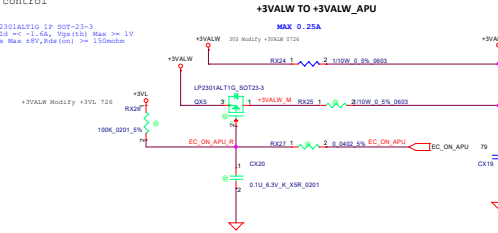


TP/B Connector



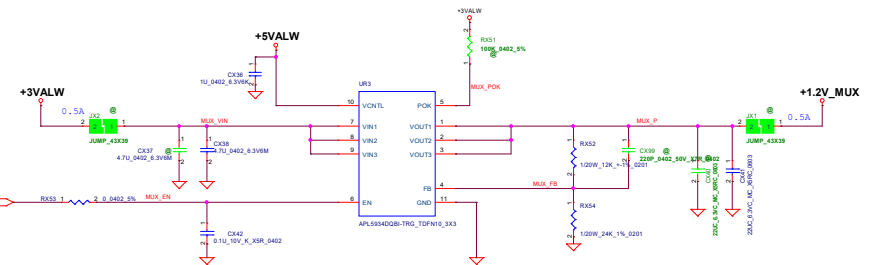
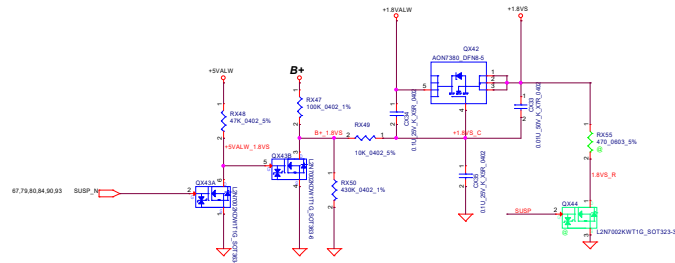
APU Power control

880000000000 LP2101ALT1G LP 20T-23-3
Load MOD B MOD 58 <= -1.6A, Vgs(15) Max >= 1V
Vds Max 20V, Vgs Max 85V, Rds(on) <= 150mΩ



tekni-indonesia

for Parade 1.2V



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Date		Version		14	

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4

3

2

1

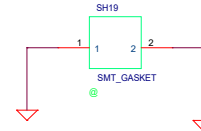
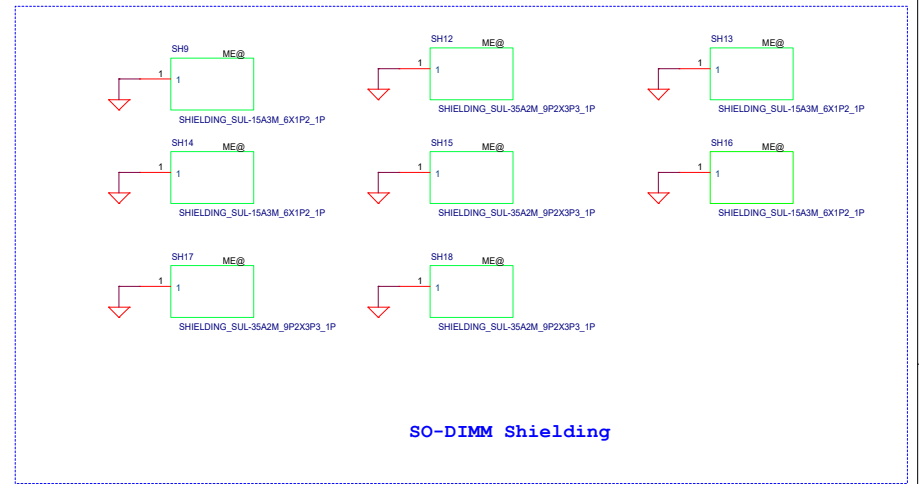
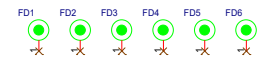
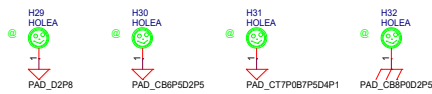
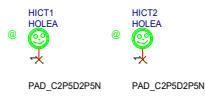
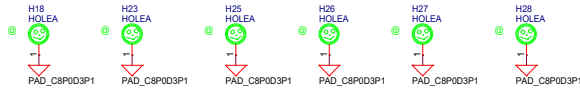
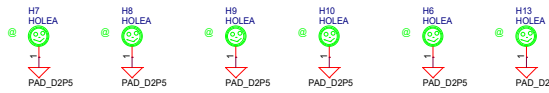
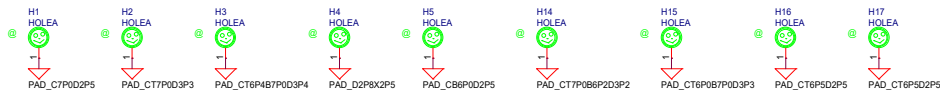
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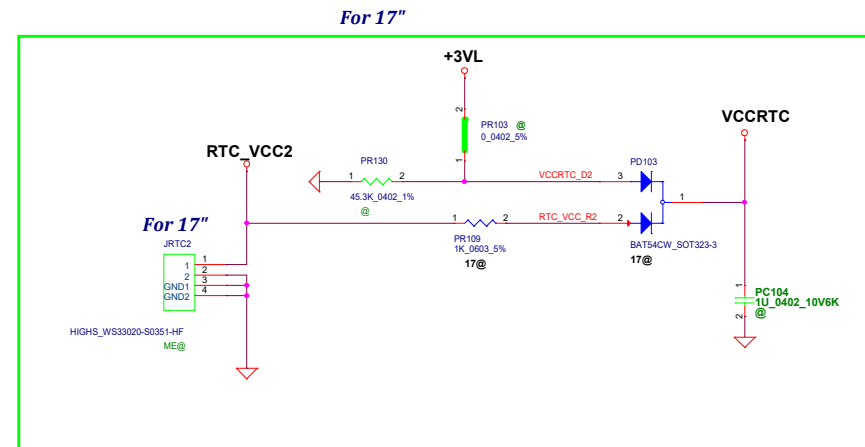
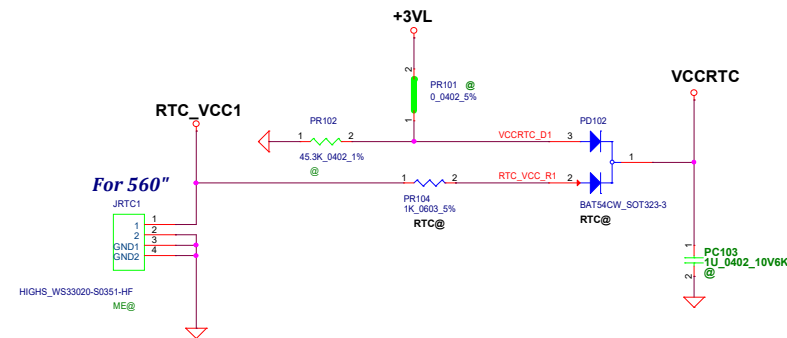
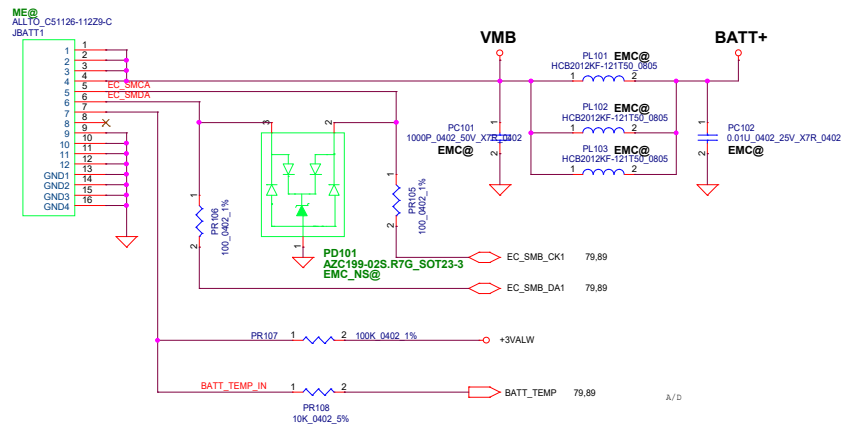
C

B

A

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<small>Thursday, November 26, 2020</small>						35
<small>110</small>						110
Date						Sheet of Rev





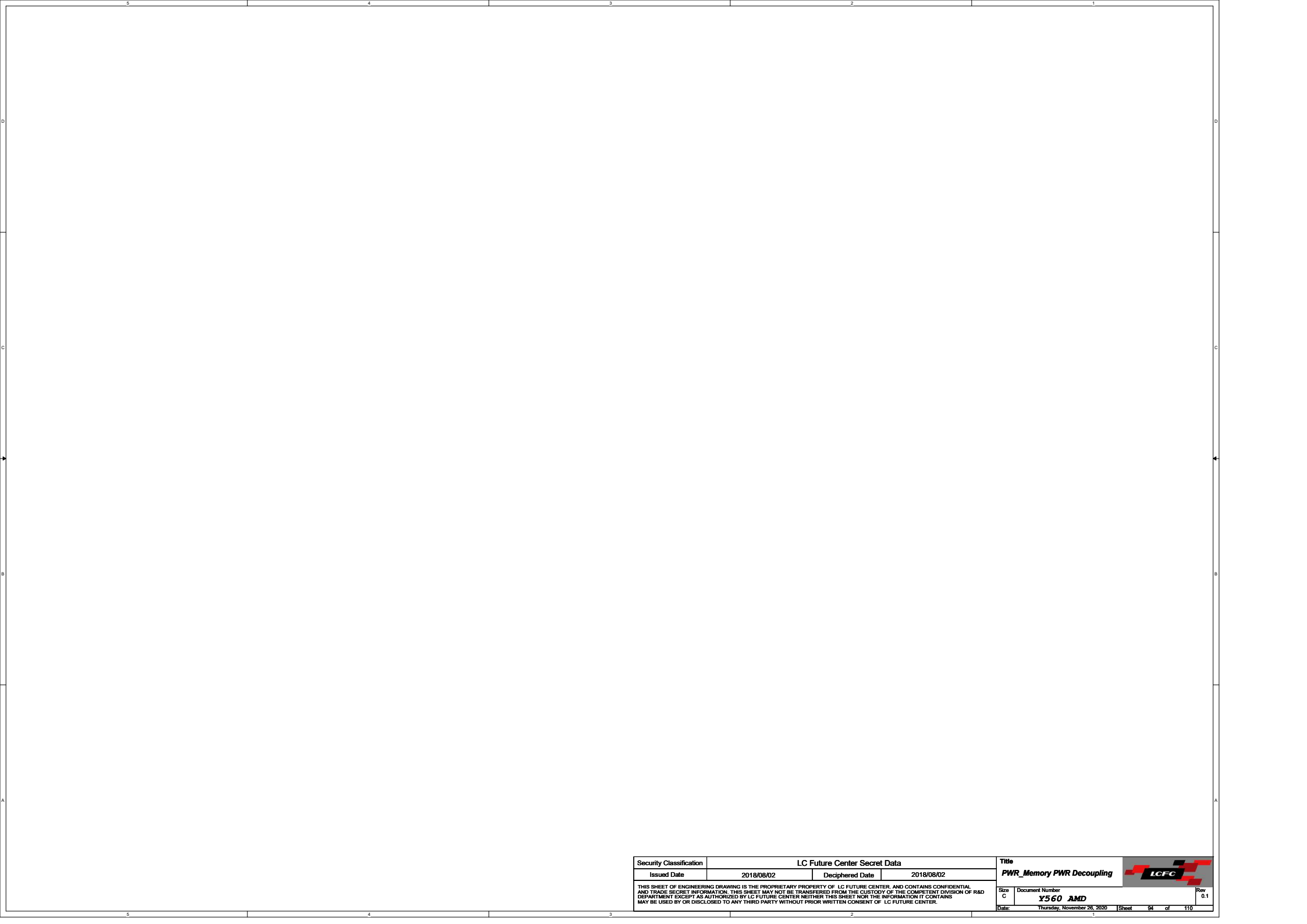
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BAT Max V=17.6V

PMON Gain=1uA/W Actuel=0.5uA/W
PR5415=15K 0.75V=100W

IchargeLIM=16.1A
IDischargeLIM=64.4A

V charge (MAX):17.6V
I charge (MAX):12A
FSW:800K

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